

IN THE CLAIMS:

Please cancel claims 1-34, without prejudice or disclaimer.

Please add new claims 35 – 57, as set forth below.

1. – 34. (Canceled)

35. (New) A multi-domain liquid crystal display device comprising:

first and second substrates facing each other;

a liquid crystal layer between said first and second substrates;

C, / a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region, said pixel region being divided into at least two portions and liquid crystal molecules in said liquid crystal layer in each portion being driven differently from each other;

a pixel electrode electrically charged through said data bus line in said pixel region;

a common-auxiliary electrode surrounding said pixel electrode on a same layer

whereon said pixel electrode is formed;

a gate insulator over said whole first substrate;

a passivation layer on said gate insulator over said whole first substrate;

a light shielding layer on said second substrate;

a color filter layer on said light shielding layer;

a common electrode on said color filter layer; and

an alignment layer on at least one substrate between said first and second substrates.

36. (New) The multi-domain liquid crystal display device according to claim 35, further comprising:

a storage electrode connecting said pixel electrode below said passivation layer and overlapping said gate bus line.

37. (New) The multi-domain liquid crystal display device according to claim 35, wherein said light-shielding layer overlaps said common-auxiliary electrode.

38. (New) The multi-domain liquid crystal display device according to claims 35, wherein said common-auxiliary electrode is electrically connect to said common electrode.

39. (New) The multi-domain liquid crystal display device according to claims 35, further comprising:

a dielectric frame for distorting electric field on said common electrode.

40. (New) The multi-domain liquid crystal display device according to claims 35, wherein said pixel electrode has an electric field inducing window inside of itself.

41. (New) The multi-domain liquid crystal display device according to claims 35, wherein said passivation layer has an electric field inducing window inside of itself.

42. (New) The multi-domain liquid crystal display device according to claims 35, wherein said gate insulator has an electric field inducing window inside of itself.

43. (New) The multi-domain liquid crystal display device according to claims 35, wherein said common electrode has an electric field inducing window inside of itself.

44. (New) The multi-domain liquid crystal display device according to claims 35, wherein said color filter has an electric field inducing window inside of itself.

45. (New) The multi-domain liquid crystal display device according to claims 35, further comprising:

an overcoat layer on said color filter layer.

46. (New) The multi-domain liquid crystal display device according to claims 45, wherein said overcoat layer has an electric field inducing window inside of itself.

47. (New) The multi-domain liquid crystal display device according to claims 35, wherein said passivation layer includes a material selected from the group consisting of BenzoCycloButene (BCB), acrylic resin, and polyimide compound.

48. (New) The multi-domain liquid crystal display device according to claims 35, wherein said common-auxiliary electrode includes a material selected from the group consisting of

Indium Tin Oxide (ITO), aluminum, molybdenum, chromium, tantalum, titanium, and an alloy thereof.

49. (New) The multi-domain liquid crystal display device according to claims 35, wherein said pixel electrode includes a material selected from the group consisting of Indium Tin Oxide (ITO), aluminum, and chromium.

50. (New) The multi-domain liquid crystal display device according to claims 35, wherein said common electrode includes Indium Tin Oxide (ITO).

51. (New) The multi-domain liquid crystal display device according to claims 35, wherein said alignment layer is divided into at least two portions, liquid crystal molecules in each portion being aligned differently from each other.

C, 52. (New) The multi-domain liquid crystal display device according to claims 51, wherein all portions of said at least two portions of said alignment layer are non-alignment-treated.

53. (New) The multi-domain liquid crystal display device according to claims 35, wherein said liquid crystal layer includes liquid crystal molecules having negative dielectric anisotropy.

54. (New) The multi-domain liquid crystal display device according to claims 35, further comprising:

a negative bi-axial film on at least one substrate.

55. (New) The multi-domain liquid crystal display device according to claims 35, wherein said liquid crystal layer includes chiral dopants.

56. (New) A multi-domain liquid crystal display device comprising:

first and second substrates facing each other;

a liquid crystal layer between said first and second substrates;

a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region, said pixel region being divided into at least two portions and liquid crystal molecules in said liquid crystal layer in each portion being driven differently from each other;

a pixel electrode electrically charged through said data bus line in said pixel region;

C,
and

a common-auxiliary electrode surrounding said pixel electrode on a same layer whereon said pixel electrode is formed.

57. (New) A multi-domain liquid crystal display device comprising:

first and second substrates facing each other;

a liquid crystal layer between said first and second substrates;

a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a

pixel region, said pixel region being divided into at least two portions and liquid crystal molecules in said liquid crystal layer in each portion being driven differently from each other;

an n-line thin film transistor at a crossing area of said gate and data bus lines;

a pixel electrode electrically charged through said data bus line in said pixel region;

and

a common-auxiliary electrode surrounding said pixel electrode on a same layer

whereon said pixel electrode is formed;

a gate insulator over said whole first substrate;

a passivation layer on said gate insulator over said whole first substrate;

a light shielding layer on said second substrate;

a color filter layer on said light shielding layer;

a common electrode on said color filter layer; and

an alignment layer on at least one substrate between said first and second substrates.
